

METHOD AND APPARATUS FOR ADJUSTING CHARACTERISTICS OF
MULTI ELECTRON SOURCE

BACKGROUND OF THE INVENTION

5 Field of the Invention

The present invention relates to a method and apparatus for adjusting the characteristics of a multi electron source having a number of surface conduction electron-emitting devices.

10 Related Background Art

Two types of electron emitting-devices are known, hot cathode devices and cold cathode devices. Known cold cathode devices include field emission devices (hereinafter described as FE),

15 metal/insulator/metal emission devices (hereinafter described as MIME) and surface conduction electron-emitting devices (hereinafter described as SCE).

The present applicants have studied a multi electron source having a number of passive-matrix
20 wired SCEs and an image display apparatus using such a multi electron source, as disclosed in Japanese Patent Application Laid-open No. 06-342636.

SCEs constituting a multi electron source have some dispersions in the electron emission
25 characteristics because of process variations. If a display apparatus is manufactured by using such SCEs, dispersions in the characteristics result in

dispersions in luminance. The present applicant
~~disclosed~~ in Japanese Patent Application Laid-open No.
10-228867 the invention that dispersions in the SCE
electron emission characteristics are removed by
5 utilizing a memory capability of the SCE electron
emission characteristics.

The present invention also relates to a
technique of leveling the characteristics of a multi
electron source by utilizing the memory capability of
10 the SCE electron emission characteristics, similar to
the above-described prior art (Japanese Patent
Application Laid-open No. 10-228867), and provides an
improved technique suitable for mass production of
electron source panels.

15 According to the prior art technique, a
characteristics leveling process incorporated in an
electron source manufacture process is likely to have
dispersions in adjustment times taken to adjust
electron-emitting devices. There is therefore the
20 possibility of dispersions in the adjustment times
taken to adjust the characteristics of electron
source panels and variations in adjusted electron
emission characteristics.

The invention provides a manufacture process
25 capable of manufacturing electron source panels
having generally the same electron emission
characteristics in generally the same process time

even if the memory performance of the electron emission characteristics of SCEs constituting a multi-electron source is different among electron-emitting devices or among electron source panels.

5 An object of the invention is therefore to provide a method and apparatus for adjusting the characteristics of multi electron sources with simple processes, the multi electron sources having generally the same electron emission characteristics
10 and adjusted in generally the same adjustment time.

SUMMARY OF THE INVENTION

 According to the invention, prior to adjusting the characteristics, initial electron emission
15 currents of all devices are measured to set a characteristics adjustment target value. By using some devices, the emission current change characteristics are measured at characteristics shift voltages. In accordance with an average of the
20 measured characteristics, a characteristics adjustment table is created. Next, by referring to the characteristics adjustment table, the pulse peak and width of the characteristics shift voltage and the number of pulses to be applied to each device are
25 determined to perform characteristics shift driving for removing a characteristics shift amount which is a difference between an initial electron emitting

current and a characteristics adjustment target value.
A change in electron emission characteristics during
the characteristics shift driving is monitored to set
again, when necessary, the characteristics shift
5 conditions including the pulse peak and width and the
number of pulses of the characteristics shift voltage.

BRIEF DESCRIPTION OF THE DRAWINGS

Figs. 1A and 1B are diagrams showing examples
10 of signals for adjusting the characteristics of SCE
according to an embodiment of the invention.

Fig. 2 is a graph showing the relation between
a shift voltage applying time and a characteristics
shift quantity.

15 Figs. 3A and 3B are graphs illustrating the
emission current characteristics at different SCE
drive voltages.

Fig. 4 is a schematic diagram showing the
structure of an apparatus for applying a
20 characteristics adjustment signal to a multi electron
source according to an embodiment of the invention.

Fig. 5 is a flow chart illustrating a process
of adjusting the characteristics of each SCE of an
electron source by using the apparatus shown in Fig.
25 4.

Fig. 6 is a flow chart illustrating the
characteristics adjustment process following the flow

chart shown in Fig. 5.

Fig. 7 is a graph showing characteristics curves illustrating a variation quantity of the electron emitting current when pulses are
5 repetitively applied to the device at each of a plurality of drive voltages.

Fig. 8 is a graph showing the range of an electron emitting current of each SCE at each of discrete characteristics voltages applied for the
10 characteristics adjustment of the apparatus shown in Fig. 4.

Fig. 9 is a diagram showing an example of a characteristics adjustment signal to be applied when it is judged that the adjustment target value cannot
15 be obtained even if pulses of the initially determined number are applied to SCE of the apparatus shown in Fig. 4.

Fig. 10 is a diagram showing an example of a characteristics adjustment signal to be applied when
20 it is judged that the current value exceeds the adjustment target value if pulses of the initially determined number are applied to SCE of the apparatus shown in Fig. 4.

Fig. 11 is a flow chart illustrating the
25 characteristics adjustment process following the flow chart shown in Fig. 6.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

The invention will be described with reference to the embodiments.

The present applicants have found that prior to
5 ordinary driving, preliminary driving disclosed in
Japanese Patent Application Laid-open Nos. 2000-
310973 and Japanese Patent Application Laid-open No.
2000-243256 is performed during a manufacture process
in order to improve the characteristics of SCEs and
10 reduce a luminance change with time. In this
embodiment, the preliminary driving and an electron
source characteristics adjustment are integrally
performed.

The preliminary driving is a process of driving
15 SCEs subjected to a stabilization operation at a
voltage V_{pre} for a predetermined period and measuring
an electric field intensity near an electron-emitting
region during this drive. Thereafter, normal image
display driving is performed at a normal drive
20 voltage V_{drv} generating a smaller electric field
intensity. As the device electron-emitting region is
driven by a large electric field intensity at the
voltage V_{pre} , the structural member which causes
instability of a change in the characteristics with
25 time is changed concentrically in a short time. It
is considered that this method can reduce the change
factors of display luminance of the display device

driven at the normal drive voltage V_{drv} .

The method of adjusting the electron emission characteristics of SCEs subjected to the preliminary driving by using the memory performance of the SCE
5 electron emission characteristics will be briefly described. The details thereof are described in the above-cited Japanese Patent Application Laid-open No. 2000-243256.

Figs. 1A and 1B are diagrams showing examples
10 of voltage waveforms of preliminary driving and characteristics adjustment driving signals applied to one device constituting a multi electron source. The abscissa represents a time and the ordinate represents a voltage (hereinafter called a device
15 voltage V_f) applied to SCE.

The drive signal is consecutive rectangular voltage pulses such as shown in Fig. 1A. The application period of a voltage pulse during the characteristics adjustment drive period is divided
20 into first to third three periods. During each period, one to thousand pulses are applied. The applied pulse peak value and the number of pulses change depending upon each device. A portion of the voltage pulse waveform shown in Fig. 1A is shown
25 enlarged in Fig. 1B.

The specific drive conditions set were a drive signal pulse width T_1 of 1 msec and a pulse period T_2

of 10 msec. In order to set the rise time T_r and fall time T_f of an effective voltage pulse applied to each device to 100 ns or shorter, the impedance of a wiring line from a drive signal source to each device
5 was sufficiently reduced to drive the device.

The device voltage V_f was set to $V_f = V_{pre}$ during the preliminary drive period, and during the characteristics adjustment period, $V_f = V_{drv}$ during the first and third periods and $V_f = V_{shift}$ during
10 the second period. These device voltages V_{pre} , V_{drv} and V_{shift} were larger than the device electron emission threshold voltage and satisfied the conditions of $V_{drv} < V_{pr} \cdot V_{shift}$. Since the electron emission threshold voltage changes with the
15 shape and material of SCE, the device drive voltages were set properly in accordance with SCE to be measured.

After all the devices are driven in the manner described above, the characteristics adjustment
20 process for a multi electron source is completed.

There is a correlation between an application time of a shift voltage during the characteristics adjustment period and a shift amount of the characteristics. Fig. 2 is a graph schematically
25 showing a correlation between an application time of a characteristics shift voltage V_{shift} and a characteristics shift amount $Shift$, the

characteristics shift voltage being equal to or higher than the electron emission threshold voltage. The X-axis of the graph indicates the shift voltage application time in a logarithmic scale and the Y-axis indicates the characteristics shift amount Shift. As shown in Fig. 2, the characteristics shift amount increases generally in direct proportion to a logarithmic value of application time of the shift voltage.

Fig. 3A is a graph showing another viewpoint of the graph of Fig. 2. As shown, as the number of applied pulses $V_f = V_{\text{shift}}$ is increased, the emission current characteristics shifts to the right. A device having the characteristics of $I_{ec} (1)$ before shift pulse application changes the characteristics to $I_{ec} (2)$ after one V_{shift} pulse is applied. The emission current characteristics curve changes to $I_{ec} (3)$ after three V_{shift} pulses are applied, the emission current characteristics curve changes to $I_{ec} (5)$ after ten V_{shift} pulses are applied, and the emission current characteristics curve changes to $I_{ec} (6)$ after one hundred V_{shift} pulses are applied. The emission current $I_{ec} (5)$ on the emission current characteristics curve takes an emission current I_{e5} at the normal drive voltage V_{drv} , and the emission current $I_{ec} (6)$ takes the emission current I_{e6} at the normal drive voltage V_{drv} . By increasing or

decreasing the number of V_{shift} pulses to be applied to a device during the second period, the emission current characteristics curve can be changed as desired so that the electron emitting current at the
5 normal drive voltage V_{drv} during the third period can be set to a particular value.

As seen from Fig. 3A, the electron emitting current of a device of a multi electron source is I_{e4} when $V_f = V_{drv}$ is applied after the preliminary
10 driving. This electron emitting current changes to $I_{e3} \rightarrow I_{e5} \rightarrow I_{e6}$ at the normal drive voltage $V_f = V_{drv}$ as the number of shift pulses $V_f = V_{shift}$ is increased. A multi electron source is constituted of a number of devices each having different
15 characteristics after the preliminary driving. The present applicant has vigorously studied how the electron emitting current changes when the characteristics shift voltage is applied to each device having different electron emission
20 characteristics after the preliminary driving. The applicant has found that the characteristics change rate after application of characteristics shift voltage is generally constant independently from the electron emission amount before shift voltage
25 application. Specifically, as shown in Fig. 3B, after the preliminary driving, the electron emitting current of a device having different initial

characteristics from the device shown in Fig. 3A having I_{e4}' at $V_f = V_{drv}$ changed to $I_{e3}' \rightarrow I_{e5}' \rightarrow I_{e6}'$ at $V_f = V_{drv}$ as the number of shift pulses $V_f = V_{shift}$ was increased. Paying attention to the I_e change ratio shown in Figs. 3A and 3B, I_e of the device (1) shown in Fig. 3A changes from I_{e4} (start) to I_{e3} (one pulse) $\rightarrow I_{e5}$ (ten pulses) $\rightarrow I_{e6}$ (one hundred pulses) as V_{shift} is applied, and the change ratio changes to $I_{e3}/I_{e4} \rightarrow I_{e5}/I_{e4} \rightarrow I_{e6}/I_{e4}$. I_e of the device (2) shown in Fig. 3B changes from I_{e4}' (start) to I_{e3}' (one pulse) $\rightarrow I_{e5}'$ (ten pulses) $\rightarrow I_{e6}'$ (one hundred pulses) as V_{shift} is applied, and the change ratio changes to $I_{e3}'/I_{e4}' \rightarrow I_{e5}'/I_{e4}' \rightarrow I_{e6}'/I_{e4}'$. The present applicant has found that the change ratios of I_{e3}/I_{e4} and I_{e3}'/I_{e4}' , I_{e5}/I_{e4} and I_{e5}'/I_{e4}' , and I_{e6}/I_{e4} and I_{e6}'/I_{e4}' are approximately equal. By utilizing this fact, the device characteristics can be adjusted by using a change curve of the same emission current characteristics even if the devices have the initial I_e currents somewhat different.

Of a number of devices, some devices have a very slow change rate after one V_{shift} voltage application and some devices have a very fast change rate after one V_{shift} voltage application as compared to the change rate on the change curve of the same emission current characteristics. Although the

number of these devices is small, the applicant has found that the device characteristics of these devices can also be adjusted by using the change curve of the same emission current characteristics by
5 applying pulses having widened or narrowed widths.

According to the invention, some devices of a multi electron source are used to acquire a change curve of the emission current characteristics after characteristics shift voltage application, and in
10 accordance with the change curve, the characteristics of the whole multi electron source are adjusted. Although the details will be given later, the characteristics of the whole electron source can be adjusted by acquiring data through selection of
15 applied shift voltage values at several discrete steps. The details will be given below.

Fig. 4 is a block diagram showing the structure of a drive circuit for changing the electron emission characteristics of each SCE constituting a display
20 panel using a multi electron source by applying a characteristics adjustment signal to each SCE. In Fig. 4, reference numeral 301 represents the display panel. In this embodiment, the display panel 301 has a plurality of SCEs passive matrix wired. It is
25 assumed that SCEs were subjected to the energization forming and activation operations and are now under a stabilization operation.

The display panel 301 has a substrate having a plurality of SCEs disposed in a matrix shape and a face plate and the like having a phosphor for emitting light in response to electrons emitted from SCEs and disposed on the substrate spaced therefrom, respectively housed in a vacuum chamber. The display panel 301 is connected to external electronic circuits via row directional wirings Dx1 to Dxn and column directional wirings Dy1 to Dym. Reference symbol 301a represents a region of the substrate having SCEs disposed in a matrix shape in the display panel 301, this portion being provided with characteristics adjustment data acquisition devices.

Reference numeral 302 represents a terminal for applying a high voltage from a high voltage source 311 to the phosphor of the display panel 301. Reference numerals 303 and 304 represent switch matrixes for selecting SCE and applying a pulse voltage by selecting a row directional wiring and a column directional wiring. Reference numerals 306 and 307 represent pulse generators for generating pulse signals Px and Py. Reference numeral 308 represents a pulse peak (height) and width value setting circuit for outputting pulse setting signals Lpx and Lpy to set the peak value and width of each pulse signal to be output from the pulse generators 306 and 307. Reference numeral 309 represents a

control circuit for controlling the whole characteristics adjustment flow and outputting data Tv to the pulse peak and width value setting circuit 308 to set the peak and width values. Reference
5 symbol 309a represents a CPU which controls the operation of the control circuit 309. The operation of CPU 309a will be later described with reference to the flow charts of Figs. 5, 6 and 11.

In Fig. 4, reference symbol 309b represents a
10 pulse setting memory for storing the characteristics of each device to adjust the characteristics of the device. Specifically, the pulse setting memory 309b stores the electron emitting current I_e of each device when the normal drive voltage V_{drv} is applied.
15 Reference numeral 309c represents a reference look-up table created by acquiring data by applying a voltage to some devices, the look-up table being referred to when the characteristics are adjusted, and the details of the look-up table being later given.
20 Reference symbol 309d represents a pulse setting memory for storing the peak and width of an application pulse suitable for each process. This memory is also used during characteristics adjustment when the pulse width is set again for an electron
25 source having a considerably different change rate. Reference numeral 310 represents a switch matrix control circuit for outputting switching signals Tx

and Ty and controlling a selection of switches of the switch matrixes 303 and 304 to select SCE to which a pulse voltage is applied.

Next, acquiring data necessary for the characteristics adjustment process will be described. In this embodiment, in order to adjust the electron emitting current of each device, the electron emission current I_e of each device is measured and stored. The details of measuring the electron emitting current I_e will be given. It is necessary for the characteristics adjustment to measure at least the electron emission current I_e flowing when the normal drive voltage V_{drv} is applied. This will be described. In response to a switch matrix control signal T_{sw} from the control circuit 309, the switch matrix control circuit 310 controls the switch matrixes 303 and 304 so that desired row and column directional wirings are selected and a desired SCE is driven.

The control circuit 309 outputs pulse peak and width value data T_v corresponding to the normal drive voltage V_{drv} to the pulse peak and width value setting circuit 309. The pulse peak and width value setting circuit 308 outputs pulse peak value data L_{px} and pulse width value data L_{py} to the pulse generators 306 and 307, respectively. In accordance with the pulse peak and width value data L_{px} and L_{py} ,

the pulse generators 306 and 307 output drive pulses P_x and P_y which are selected by the switch matrixes 303 and 304 and applied to the device. The drive pulses P_x and P_y having a half amplitude of the normal drive voltage V_{drv} (peak value) and opposite polarities is applied to the device. At the same time, a predetermined voltage is applied from the high voltage source 311 to the phosphor of the display panel 301.

10 According to the electron emission characteristics of SCE, as the device voltage equal to or higher than the threshold voltage is applied, the electron emitting current I_e increases abruptly, whereas the device voltage smaller than the threshold voltage is applied, the electron emission current I_e is hardly detected. Namely, SCE is a nonlinear device having a definite threshold voltage V_{th} . Therefore, as the drive pulses P_x and P_y having an amplitude of a half V_{drv} and opposite polarities are applied, electrons are emitted only from the device selected by the switch matrixes 303 and 304. The electron emitting current I_e of the device driven by the drive pulses P_x and P_y is measured with a current detector 305.

25 The process flow of adjusting the electron emission characteristics of each SCE constituting a

multi electron source will be described with
reference to the flow charts of Figs. 5, 6 and 11.
In this embodiment, the preliminary driving and
characteristics adjustment driving are performed
5 integrally. Both the drive processes will be
described.

The process flow includes a first stage I (flow
chart shown in Fig. 5, corresponding to the
preliminary drive period and first period of the
10 characteristics adjustment period shown in Fig. 1A),
a second stage II (flow chart shown in Fig. 6,
corresponding to the second and third periods of the
characteristics adjustment period shown in Fig. 1A)
and a third stage III (flow chart shown in Fig. 11,
15 corresponding to the second and third periods of the
characteristics adjustment period shown in Fig. 1A).
At the first stage I, after the preliminary drive
voltage V_{pre} is applied to all devices of the display
panel 301, the electron emission characteristics when
20 the normal drive voltage V_{drv} is applied are measured
to set a target standard electron emitting current
 I_{e-t} for the characteristics adjustment. At the
second stage II, the look-up table is created by
alternately applying the characteristics shift
25 voltage V_{shift} and normal drive voltage V_{drv} to each
of some devices in the region 301a hardly obstructing
an image display and by detecting an electron

emitting current variation quantity. At the third stage III, the pulse waveform signal having the characteristics shift voltage V_{shift} is applied in accordance with the characteristics adjustment look-up table and the electron emission characteristics are measured at the normal drive voltage C_{drv} in order to judge whether the characteristics adjustment is completed.

First, the first stage (flow chart of Fig.5) will be described. At Step S11, in response to an output of the switch matrix control signal T_{sw} , the switch matrix control circuit 310 switches the switch matrixes 303 and 304 to select one device of the display panel 301. At Step S12 the pulse peak and width value data T_v of a pulse signal to be applied to the selected device and stored in advance in the pulse setting memory 309d is output to the pulse peak and width value setting circuit 308. The peak of a measurement pulse is the preliminary drive voltage $V_{pre} = 16 \text{ V}$ and the pulse width is 1 msec. At Step S13 the pulse generators 306 and 307 apply a pulse voltage of the preliminary drive voltage V_{pre} to the device selected at Step S11 via the switch matrixes 303 and 304. At Step S14 in order to evaluate the electron emission characteristics of the device subjected to the preliminary drive and driven at the normal drive voltage V_{drv} , the normal drive voltage

Vdrv = 14.5 V and pulse width of 1 msec preset in the pulse setting memory 309d are set as the pulse peak and width data Tv of a pulse signal to be applied to the selected device. At Step S15 a pulse signal of
5 the normal drive voltage Vdrv is applied to the device selected at Step S11. At Step S16 the electron emitting current Ie at Vdrv is stored in the memory 309b for the characteristics adjustment.

It is checked at Step S17 whether the
10 measurements are completed for all SCEs of the display panel 301. If not, the flow advances to Step S18 whereat the switch matrix control signal Tsw for selecting the next device is set to thereafter return to Step S11. If it is judged at Step S17 that the
15 measurements are completed for all SCEs, then at Step S19 the electron emitting currents Ie of all SCEs of the display panel 301 at the normal drive voltage Vdrv are compared to set the target standard electron emitting current Ie-t.

20 The target standard electron emitting current Ie-t was set in the following manner.

As shown in Fig. 3A, upon application of the characteristics shift voltage, the Ie-Vf curve shifts to the right in any of the devices. Therefore, the
25 target value is set to a small one among Ie's at Vdrv. However, if the target value is set too small, an average electron emission amount of a multi electron

source after the characteristics adjustment lowers too much. In this embodiment, electron emitting current values of all devices were statistically processed to calculate an average electron emitting
5 current I_{e-ave} and a standard deviation $\sigma-I_e$. The target standard electron emitting current I_{e-t} was set to $I_{e-t} = I_{e-ave} - \sigma-I_e$.

By setting the target standard electron emitting current I_{e-t} in the above manner, the
10 electron emission amount of each device can be made level without greatly lowering the average electron emitting current of a multi electron source after the characteristics adjustment.

Next, the second stage II (flow chart of Fig.
15 6) will be described.

In creating the look-up table, characteristics shift voltage values at four discrete levels (V_{shift1} to V_{shift4}) were selected and the characteristics shift amount at each voltage was measured. The range
20 of the characteristics shift voltage is $V_{shift} \geq V_{pre}$ as described earlier, and properly set in accordance with the shape and material of SCE. The characteristics adjustment can be performed generally by dividing into several steps at an interval of
25 about 1 V.

First, with reference to the flow chart shown in Fig. 6, description is made for a process of

measuring a variation quantity of the device emission current I_e when the characteristics shift voltages of V_{shift1} , V_{shift2} , V_{shift3} and V_{shift4} (1 to 100 pulses) are applied to a plurality of devices.

- 5 At Step S21 the region of a plurality of SCEs to be applied with each of the characteristics shift voltages, the number of devices, each characteristics shift voltage value, a pulse width and the number of pulses are set. The region in the display panel 301
- 10 of a plurality of devices to be applied with each of the four characteristics shift voltages was set to the region 301a where an image display is hardly obstructed, and the number of devices was set to twenty devices per each characteristics shift voltage.
- 15 At Step S22, the switch matrix control signal T_{sw} is output so that the switch matrix control circuit 310 switches the switch matrixes 303 and 304 to select one device of the display panel 301. At Step S23 the pulse peak and width value data T_v of a pulse signal
- 20 to be applied to the selected device and preset in the pulse setting memory 309d is output to the pulse peak and width value setting circuit 308. The peak of the characteristics shift voltage is either the preliminary drive voltage $V_{pre} = 16 \text{ V}$, a
- 25 characteristics shift voltage $V_{shift1} = 16.25 \text{ V}$, a characteristics shift voltage $V_{shift1} = 16.5 \text{ V}$, a characteristics shift voltage $V_{shift1} = 16.75 \text{ V}$, or a

characteristics shift voltage $V_{shift1} = 17$ V, and the pulse width is 1 msec for all cases. At Step S24, the pulse generators 306 and 307 apply the preliminary drive voltage V_{pre} as the first
5 characteristics shift voltage to the device selected at Step S21 via the switch matrixes 303 and 304.

At Step S25 in order to evaluate the electron emission characteristics of the device subjected to the application of the characteristics shift voltage
10 of the normal drive voltage V_{drv} , the normal drive voltage $V_{drv} = 14.5$ V and pulse width of 1 msec preset in the pulse setting memory 309d are set as the pulse peak and width data T_v of a pulse signal to be applied to the selected device. At Step S26 a
15 pulse signal of the normal drive voltage V_{drv} is applied to the device selected at Step S22. At Step S27 the electron emitting current I_e at V_{drv} is stored in the memory 309b as electron emission amount change data corresponding to the number of applied
20 characteristics shift voltage pulses. It is checked at Step S28 whether the characteristics shift voltage is applied to the device selected at Step S22 a predetermined number of times. If not, the flow returns to Step S23.

25 If it is judged at Step S28 that the characteristics voltage is applied a predetermined number of times, the flow advances to Step S29

whereat it is checked whether the electron emission amount change measurements are completed for the predetermined number of devices. If not, the flow advances to Step S30 whereat the switch matrix control signal Tsw for selecting the next device is set to thereafter return to Step S22. If it is judged at Step S29 that the measurements are completed for the predetermined number of devices, then variation quantities of the electron emitting current when each of the five characteristics shift voltages Vshift0 (=Vpre), Vshift1, Vshift2, Vshift3 and Vshift4 is applied (1 to 100 pulses) to the predetermined number of devices, are plotted in a graph.

Fig. 7 is a graph showing the variation quantities (average values) of the electron emitting current when each of the five characteristics shift voltages Vshift0 (=Vpre), Vshift1, Vshift2, Vshift3 and Vshift4 is applied (0 to 100 pulses) to the predetermined number of devices. The device electron emitting current value is measured at the normal drive voltage (Vdrv) after each time one pulse of each characteristics shift voltage is applied. The relation between the five characteristics shift voltages is $V_{\text{shift4}} > V_{\text{shift3}} > V_{\text{shift2}} > V_{\text{shift1}} > V_{\text{pre}}$.

As shown in Fig. 7, as the number of characteristics shift voltage application times is

increased or as the characteristics shift voltage is raised, the variation quantity of the device characteristics becomes large, i.e., the adjustment amount becomes large. The characteristics of a whole
5 multi electron source are adjusted by the following two steps by using the characteristics change curves shown in Fig. 7.

(1) In accordance with the target standard emission current I_{e-t} set by the I_e measurement
10 results obtained as illustrated in Fig. 5, a characteristics shift voltage range and an average number of applied pulses are set. Namely, this step creates the look-up table for the characteristics adjustment.

15 (2) In accordance with the values set at (1), the characteristics shift voltage for each device is set. By repeating the characteristics shift voltage application and electron emitting current characteristics measurement, the characteristics are
20 shifted to the target value. This corresponds to the stage III (flow chart of Fig. 11, corresponding to the second and third periods of the characteristics adjustment period shown in Fig. 1A) whereat the pulse signal of the characteristics shift voltage V_{shift} is
25 applied in accordance with the look-up table for the characteristics adjustments and the normal drive voltage V_{drv} is applied to measure the electron

emission characteristics in order to judge whether the characteristics adjustment is completed.

As described earlier, there are some electron sources, although not many, which have a considerably different change rate relative to the number of applied pulses illustrated in the characteristics change curves of Fig. 7. The characteristics of even such electron sources can be adjusted by incorporating a countermeasure to be described later into the characteristics adjustment steps (1) and (2) applicable to most of electron sources.

The details of the steps (1) and (2) will be given.

(1) The maximum adjustment rate D_{\max} is obtained by the following equation:

$$D_{\max} = I_{e-t} / I_{e \max}$$

where $I_{e \max}$ is the maximum current value measured as illustrated in Fig. 5 and I_{e-t} is the target current I_{e-t} . For example, assuming that the target $I_{e-t} = 0.9 \mu A$ and $I_{e \max} = 1.2 \mu A$, it is necessary that $D_{\max} = 0.75$. In this case, it can be seen from Fig. 7 that all devices cannot be adjusted if only one pulse of even the largest shift voltage V_{shift4} is applied. As the number of characteristics shift voltage application pulses increases, it is not preferable because the characteristics adjustment process time prolongs. In this embodiment, therefore, the

characteristics are adjusted with an average of ten pulses. The process time can be estimated from a product of a ten-pulse application time and the number of devices having the target I_{e-t} or larger.

5 Adjustment rates D_0 to D_4 of E_i when ten pulses are applied are read from Fig. 7.

 An electron emitting current upper limit I_{e-u} of a device at the normal drive (V_{drv}) immediately after an initial one pulse of the preliminary drive
10 (V_{pre}) is applied which pulse is expected to obtain the target electron emitting current I_{e-t} immediately after 10 pulses of the characteristics shift voltage V_{shift} are applied, can be given by the following equation:

15 $I_{e-u} = I_{e-t}/D$

 Namely, assuming that the adjustment rate when ten pulses of the characteristics shift voltage V_{shift1} are applied is D_1 , an electron emitting current upper
limit I_{e-u1} at the normal drive (V_{drv}) after one
20 pulse of the preliminary drive (V_{pre}) is applied is given by:

$$I_{e-u1} = I_{e-t}/D_1$$

 Similarly, assuming that the adjustment rate when ten pulses of the characteristics shift voltage V_{shift2}
25 are applied is D_2 , an electron emitting current upper limit I_{e-u2} at the normal drive (V_{drv}) after one pulse of the preliminary drive (V_{pre}) is applied is

given by:

$$I_{e-u2} = I_{e-t}/D2$$

Assuming that the adjustment rate when ten pulses of the characteristics shift voltage V_{shift3} are applied is $D3$, an electron emitting current upper limit I_{e-u3} at the normal drive (V_{drv}) after one pulse of the preliminary drive (V_{pre}) is applied is given by:

$$I_{e-u3} = I_{e-t}/D3$$

10 Assuming that the adjustment rate when ten pulses of the characteristics shift voltage V_{shift4} are applied is $D4$, an electron emitting current upper limit I_{e-u4} at the normal drive (V_{drv}) after one pulse of the preliminary drive (V_{pre}) is applied is given by:

$$I_{e-u4} = I_{e-t}/D4$$

Assuming that the adjustment rate when ten pulses of the characteristics shift voltage V_{shift0} are applied is $D0$, an electron emitting current upper limit I_{e-u0} at the normal drive (V_{drv}) after one pulse of the preliminary drive (V_{pre}) is applied is given by:

$$I_{e-u0} = I_{e-t}/D0$$

A look-up table for the characteristics adjustment created from these electron emission upper limits is shown in Fig. 8. As shown in Fig. 8, an electron emitting current range of a device at the normal drive (V_{drv}) after one pulse of the

preliminary drive (V_{pre}) is applied, for the characteristics adjustment upon application of the characteristics shift voltage V_{pre} ($= V_{shift0}$), is from the target I_{e-t} to I_{e-u1} . Similarly, an
5 electron emitting current range of a device at the normal drive (V_{drv}) after one pulse of the preliminary drive (V_{pre}) is applied, for the characteristics adjustment upon application of the characteristics shift voltage V_{shift1} , is from I_{e-u1}
10 to I_{e-u2} . An electron emitting current range of a device at the normal drive (V_{drv}) after the preliminary drive (V_{pre}), for the characteristics adjustment upon application of the characteristics shift voltage V_{shift2} , is from I_{e-u2} to I_{e-u3} . An
15 electron emitting current range of a device at the normal drive (V_{drv}) after the preliminary drive (V_{pre}), for the characteristics adjustment upon application of the characteristics shift voltage V_{shift3} , is from I_{e-u3} to I_{e-u4} . An electron
20 emitting current range of a device at the normal drive (V_{drv}) after the preliminary drive (V_{pre}), for the characteristics adjustment upon application of the characteristics shift voltage V_{shift4} , is larger than I_{e-u4} . If the electron emitting current at the
25 normal drive voltage V_{drv} after the preliminary drive V_{pre} is larger than I_{e-ue} , V_{shift4} was applied.

Assuming for example that the adjustment rates

after ten pulses of each characteristics shift
voltage are applied are $D0 = 0.9$, $D1 = 0.81$, $D2 =$
 0.72 , $D3 = 0.6$ and $D4 = 0.5$ and that the target I_e-t
 $= 0.9 \mu A$ and the maximum $= 1.55 \mu A$, then I_e ranges of
5 the device applied with respective characteristics
shift voltages are $0.9 < I_e \leq 1.0 \mu A$ (@Vshift0), 1.0
 $< I_e \leq 1.11 \mu A$ (@Vshift1), $1.11 < I_e \leq 1.25 \mu A$
(@Vshift2), $1.25 < I_e \leq 1.5 \mu A$ (@Vshift3), and $1.5 <$
 I_e (@Vshift4).

10 Description is made for a method of dealing
with an electron source having devices with a
considerably different change rate relative to the
number of applied pulses as illustrated in the
characteristics change curves shown in Fig. 7. As
15 described earlier, the electron emission
characteristics of most of electron sources were able
to be set to almost the target I_e-t at ten pulses or
smaller per device, by creating the look-up table
from the characteristics change curves shown in Fig.
20 7 assuming that the average number of applied pulses
is ten pulses and by determining the characteristics
shift voltage from this table. In the
characteristics adjustment to be described later, the
maximum number of pulses to be applied is set to
25 twenty pulses which is twice the average number of
applied pulses. Devices which were not able to have
a value near the target I_e-t although the

characteristics adjustment was performed include those devices unable to have the target I_e-t even if the maximum number of twenty pulses were applied and those devices which had a value much smaller than the
5 target I_e-t during the characteristics adjustment.

Namely, those devices are the devices with a considerably different change rate relative to the number of applied pulses as illustrated in the characteristics change curves shown in Fig. 7.

10 Description is made for a method of reducing the number of such devices or electron sources whose characteristics adjustment cannot be completed. First, in order to estimate whether there are such devices whose characteristics adjustment cannot be
15 completed, an electron emitting current I_e measured by applying an initial characteristics shift voltage and thereafter applying the normal drive voltage C_{drv} is compared with an electron emitting current I_e at the estimated change rate. The lower limit of the
20 estimated change rate is the change rate $D-11$ at which it cannot be expected that the device can have the target I_e-t even the maximum number of twenty pulses are applied. The upper limit of the estimated change rate is the change rate $D-u1$ at which it can
25 be expected that the device has a value lower than the target I_e-t at the second pulse application. The characteristics change curves shown in Fig. 7 can be

represented by a logarithmic scale. Therefore, for example, the characteristics change curve at the shift voltage V_{shift0} and at the pulse width of 1 msec can be represented by:

5
$$y = A_0 \cdot \log x + B_0$$

where x is the number of pulses, y is the I_e variation quantity, A_0 and B_0 are constants.

The lower limit of the change rate D_{-110} can be expressed in the following manner. If the change
10 rate upon application of the initial characteristics shift voltage is the lower limit change rate D_{-110} , the characteristics change curve is given by:

$$\begin{aligned} y &= A_0 \cdot \log 1 + D_{-110} \\ &= D_{-110} \end{aligned}$$

15 The change rate upon application of twenty pulses on this characteristics change curve is given by:

$$y = A_0 \cdot \log 20 + D_{-110}$$

If this value is higher than the change rate upon application of ten pulses on the initially set
20 characteristics curves, it cannot be expected that the characteristics adjustment has the target I_e -t upon application of the maximum number of twenty pulses, so that:

$$A_0 \cdot \log 20 + D_{-110} < A_0 \cdot \log 10 + B_0$$

25 The lower limit change rate D_{-110} can therefore be given by:

$$D_{-110} < A_0 \cdot \log 10 + B_0 - A_0 \cdot \log 20$$

$$< B0 - A0 \cdot \log 2 \cong B0 - 0.3 \cdot A0$$

If the change rate upon application of the initial pulse voltage is smaller than the lower limit change rate $D-l10$, it can be expected that the target $Ie-t$ can be obtained within the maximum number of twenty pulses. However, if the change rate is larger than the lower limit change rate $Dl10$, it cannot be expected that the target $Ie-t$ can be obtained. If the change rate is larger than the lower limit change rate $Dl10$, as shown in the second period of the characteristics adjustment period of Fig. 9, the pulse width of the second and succeeding pulse signal is broadened. This means that the variation quantity at each pulse application is made large, so that the target $Ie-t$ can be obtained before and after the average number of applied pulses. In this embodiment, the pulse width of the second and succeeding pulses was set to 2 msec which is a twofold of 1 msec.

The upper limit of the change rate $D-u10$ can be expressed in the following manner. If the change rate upon application of the initial characteristics shift voltage is the upper limit change rate $D-u10$, the characteristics change curve is given by:

$$\begin{aligned} y &= A0 \cdot \log 1 + D-u10 \\ &= D-u10 \end{aligned}$$

The change rate upon application of two pulses on this characteristics change curve is given by:

$$y = A0 \cdot \log 2 + D-u10$$

If this value is lower than the change rate upon application of ten pulses on the initially set characteristics curves, it cannot be anticipated that the characteristics adjustment has a value lower than the target $Ie-t$ upon application of the second pulse, so that:

$$A0 \cdot \log 2 + D-u10 > A0 \cdot \log 10 + B0$$

The upper limit change rate $D-u10$ can therefore be given by:

$$D-u10 > A0 \cdot \log 10 + B0 - A0 \cdot \log 2$$

$$> B0 + A0 \log 5 \cong B0 - 0.7 \cdot A0$$

If the change rate upon application of the initial pulse voltage is smaller than the upper limit change rate $D-u10$, as shown in the second period of the characteristics adjustment period of Fig. 10, the width of the second and succeeding pulses is narrowed. This means that the variation quantity at each pulse application is made large, so that the target $Ie-t$ can be obtained before and after the average number of applied pulses. In this embodiment, the pulse width of the second and succeeding pulses was set to 0.1 msec which is one tenth of 1 msec.

Similarly, the lower change rates $D-l11$ to $D-l14$ and upper change rate $D-u11$ to $D-u14$ can be calculated for the characteristics shift voltage values $Vshift1$ to $Vshift4$, and the pulse width when

the change rate becomes higher than the lower limit change rate and the pulse width when the change rate becomes lower than the upper change rate can be properly set. In order to process the device having
5 a considerably different change rate relative to the number of applied pulses as illustrated in the characteristics change curves of Fig. 7, when the look-up table is created, the lower limit change rates D-110 to D-114 and upper change rates D-u10 to
10 D-u14 at the shift voltages Vshift0 to Vshift4 are calculated, and the pulse width when the change rate becomes higher than the lower limit change rate and the pulse width when the change rate becomes lower than the upper change rate are properly set. These
15 values are stored in the pulse setting memory 309d.

Next, the stage III (flow chart of Fig. 11) will be described.

First, at Step S51 the maximum number of pulses per each SCE of the display panel 301 is set which
20 pulses are applied for the characteristics adjustment to SCE. The maximum number of pulses to be applied was set to twenty pulses which are a twofold of the average number of applied pulses. Next, at Step S52 the switch matrix control signal Tsw is output to the
25 switch matrix control circuit 310 to switch the switch matrixes and select one SCE of the display panel 301. At Step S53, the electron emitting

current of the selected device subjected to the preliminary driving and then applied with the normal drive voltage V_{drv} is read. At Step S54 the characteristics adjustment look-up table is read. At
5 Step S55 the electron emitting current of the selected device read at Step S53 is compared with the characteristics adjustment target I_{e-t} to thereby judge whether the characteristics adjustment is performed. If the electron emitting current of the
10 selected device read at Step S53 is equal to or smaller than the characteristics adjustment target I_{e-t} , the characteristics adjustment is not performed and the flow advances to Step S66.

If the electron emitting current of the
15 selected device read at Step S53 is larger than the characteristics adjustment target I_{e-t} , the pulse width and one of the characteristics shift voltages V_{shift0} to V_{shift4} corresponding to the electron emitting current of the device and selected by
20 referring to the value of the look-up table read at Step S54 are set to the pulse setting memory 309d. At Step S56 the pulse peak and width data T_v of the pulse signal set to the pulse setting memory 309d and applied to the selected device is output to the pulse
25 peak and width setting circuit 308. At Step S57, the pulse generators 306 and 307 apply the pulse signal of one of the characteristics shift voltages V_{shift0}

to Vshift4 to SCE selected at Step S52 via the switch matrixes 303 and 304. For example, assuming that the electron emitting current of SCE selected at Step S52 is I_{e-p} in the following range:

5 $I_{e-u2} < I_{e-p} \leq I_{e-u3}$

then the characteristics shift voltage is Vshift2 according to the characteristics adjustment look-up table shown in Fig. 8.

At Step S58 in order to evaluate the
10 characteristics of the device subjected to the characteristics adjustment and driven at a lowered voltage of the normal drive voltage Vdrv, the normal drive voltage Vdrv and pulse width of 1 msec are set as the pulse peak and width data Tv of the pulse
15 signal to be applied to the selected device and preset to the pulse setting memory 309d. At Step S59 a pulse signal of the normal drive voltage Vdrv is applied to the device selected at Step S52. The electron emitting current at this time is measured
20 and stored in the memory at Step S60. At Step S61 it is checked whether the electron emitting current measured at Step S60 is not equal to or lower than the characteristics adjustment target I_{e-t} , the flow advances to Step S62 whereat it is checked whether
25 the number of applied pulses is single. If the electron emitting current measured at Step S60 is equal to or lower than the characteristics adjustment

target I_e-t , the characteristics adjustment is not performed to thereafter advance to Step S66.

At Step S62 it is checked whether the number of applied pulses is single. If single, the flow
5 advances to Step S63. If it is the second or succeeding pulse, the flow advances to Step S65 whereat it is checked whether the cumulative number of applied pulses reaches the maximum number of pulses to be applied for the characteristics
10 adjustment driving. At Step S63 the lower limit change rate and upper limit change rate corresponding to the characteristics shift voltage applied to the selected device are read from the pulse setting memory 309d in order to judge whether the selected
15 device is a device having a considerably different change rate relative to the number of applied pulses as illustrated in the characteristics change curves shown in Fig. 7. The electron emitting current of the selected device subjected to the preliminary
20 driving and then applied with the normal drive voltage V_{dr} , multiplied by the lower limit change rate is set as the lower I_e value, and multiplied by the upper limit change rate is set as the upper I_e value. These values are compared with the electron
25 emitting current measured at Step S60. At Step S64, if the electron emitting current measured at Step S60 is larger than the lower limit I_e value, the width of

the pulse signal to be applied is revised to 2 msec which is a twofold of 1 msec, if it is smaller than the upper limit I_e value, the width of the pulse signal to be applied is revised to 0.1 msec which is
5 one tenth of 1 msec, or if it is between the lower and upper limit I_e values, the width of the pulse signal to be applied is maintained at 1 msec to thereafter advance to Step S56 for the application of the second pulse.

10 At Step S65 it is checked whether the cumulative number of applied pulses to the selected device including the second and succeeding pulses reaches the maximum number of pulses to be applied for the characteristics adjustment driving. If not
15 reach, the flow advances to Step S56 to apply a pulse similar to the previous pulse application, whereas if reaches, the flow advances to Step S66. At Step S66 it is checked whether all SCEs of the display panel were subjected to the characteristics adjustment. If
20 not, the flow advances to Step S67 whereat the next device is selected, the switch matrix control signal T_{sw} is output, and thereafter returns to Step S52. If it is judged at Step S66 that all devices were subjected to the characteristics adjustment, then the
25 flow is terminated. In this state, the electron emitting currents of all devices are leveled. The step (2) is therefore terminated. The process time

is approximately a product of the number of devices having the initial I_e larger than the target I_{e-t} and the time taken to apply ten pulse shift voltages.

In addition to the method of dealing with the
5 electron source having a considerably different
change rate relative to the number of applied pulses
as illustrated in the characteristics change curves
of Fig. 7, another method may be used by which one of
the characteristics shift voltage V_{shift0} to V_{shift4}
10 applied to the electron source having a considerably
different change rate is raised or lowered to apply
it to the second and succeeding pulses to make the
change rate have a value near to the estimated change
rate and reach the target I_{e-t} .

15 In this embodiment, the characteristics
adjustment look-up table is created for each display
panel 301 and the characteristics adjustment is
performed by using the characteristics adjustment
look-up table. If the characteristics adjustment is
20 performed for display panels of the same lot by using
the same target electron emitting current I_{e-t} of SCE,
the characteristics adjustment look-up table may be
created only for the first display panel. In this
case, for the second and succeeding display panels,
25 if the measurement results of the electron emission
characteristics at the normal drive voltage V_{drv}
after the preliminary drive voltage V_{pre} is applied

to all SCEs of the display panel 301 fall in a range capable of setting the current value to the target electron emitting current I_{e-t} , then the characteristics adjustment is possible by using the characteristics adjustment look-up table for the first display panel, without obtaining data for all the characteristics change curves shown in Fig. 7 but obtaining only some confirmation data. In this manner, the process time for the characteristics adjustment of the second and succeeding display panes can be shortened.

In this embodiment, the electron emitting currents are measured and the characteristics adjustment is performed to level the electron emitting currents. Instead, the luminance of the phosphor which emits light upon reception of electrons from SCE may be measured and the characteristics adjustment is performed to level the luminance. Namely, the luminance of the phosphor which emits light upon reception of electron from a device when the device is driven, is measured with a CCD sensor or the like. The measured luminance is converted into a value corresponding to the electron emitting current to level the electron emitting currents.

In this embodiment, although the devices in the image display area 301a of the display panel is used,

dummy devices not driven during an image display may be formed to acquire data from these dummy devices.

As described so far, according to the invention, for an electron generating apparatus having a multi
5 electron source with a plurality of SCEs, a characteristics adjustment process time for each SCE can be leveled with simple structures. In mass production, variations of the electron emission characteristics of electron source panels after the
10 characteristics adjustment and variations of characteristics adjustment times can be suppressed and the management of manufacture processes can be made easy.